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I915P-A7 Combo

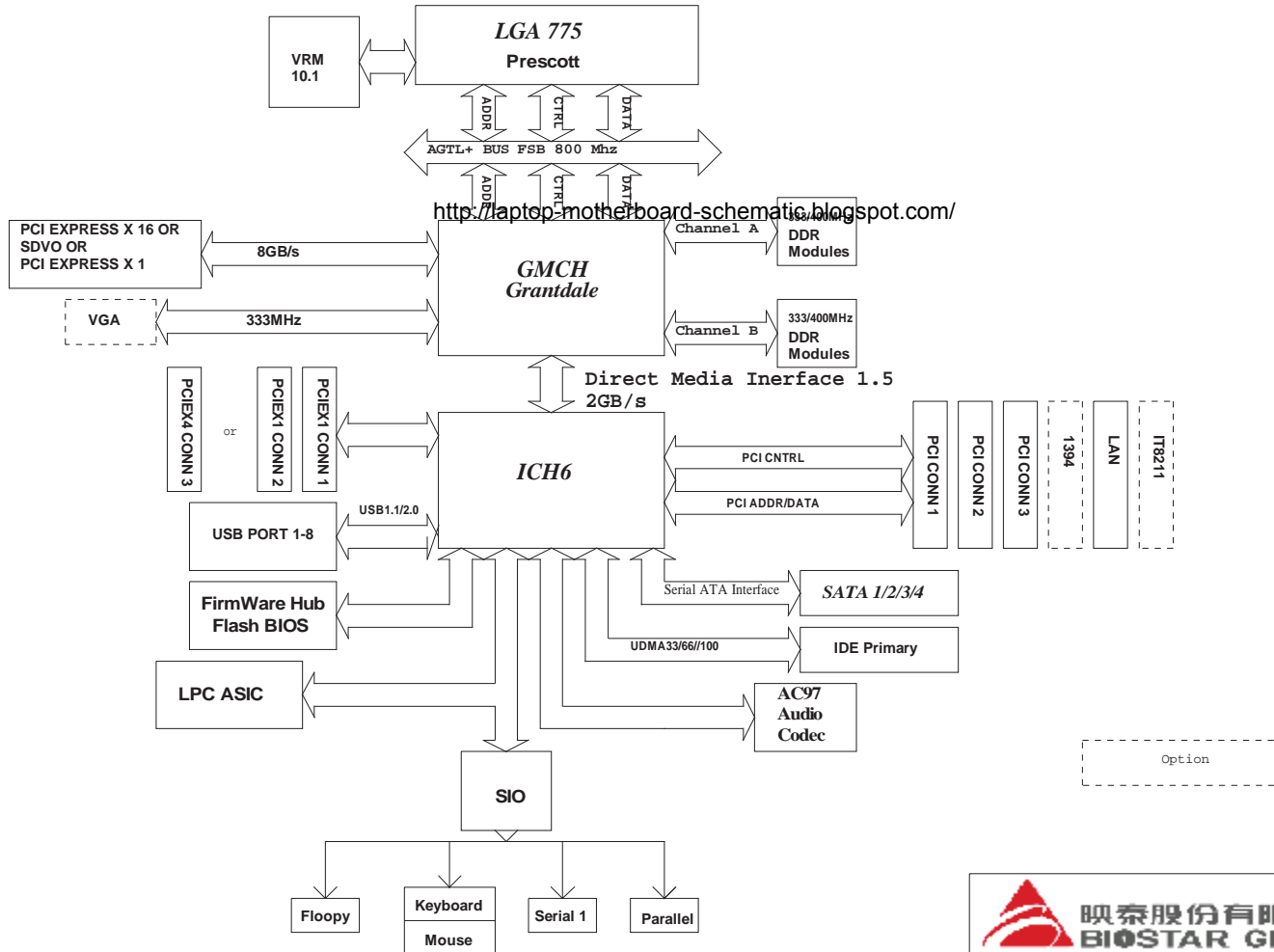
Grantdale

VER:1.0

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Cover Sheet					
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BLOCK DIAGRAM



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Title: **Block Diagram**


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PCI Solt	IDE SEL	INT#	GNT#	REQ#	CLOCK
1	16	F	2	2	PIN 12
2	17	G	3	3	PIN 13
3	18	C	4	4	PIN 14

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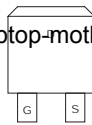
Chips	IDE SEL	INT#	GNT#	REQ#	CLOCK	GPIO
PATA	19	D	1	1	PIN 15	
ICH6					PIN 7	
S I/O					PIN 9	13
BIOS					PIN 8	
LAN	21	E	0	0	PIN 15	
ANTI-POP						25
DIMM-OV						22 & 23
CPU-OV						21 & 20
SATA-LED						33
S I/O	48MHz				PIN 25	
1394A	20	H	5	5		

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22U/25DE	5*7 mm
100U/16DE	6.3*11 mm
220U/10DE	6.3*11 mm
470U/16DE	8*11 mm
1000U/10DE	8*14 mm
1500U/16DE	10*25 mm
3300U/25DE	10*25 mm



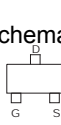
TO-263
B55QS03



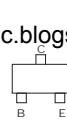
TO-263
2SK3296



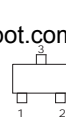
TO-252
20N03
TM3055TL-S
45N03
FDD6030L



SOT-23
2N7002
SI2303S
SI2301S



SOT-23
2N3904
2N3906
MMBT2907A



SOT-23
BAT54C
BAT54S



TO-92
LM431
78L05-D
LM432




TO-92
2N2222A
2N2097A



TO-92
HSD882-D

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Component Size		
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Title

Change List

Size

Document Number

Rev

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1.0

Date

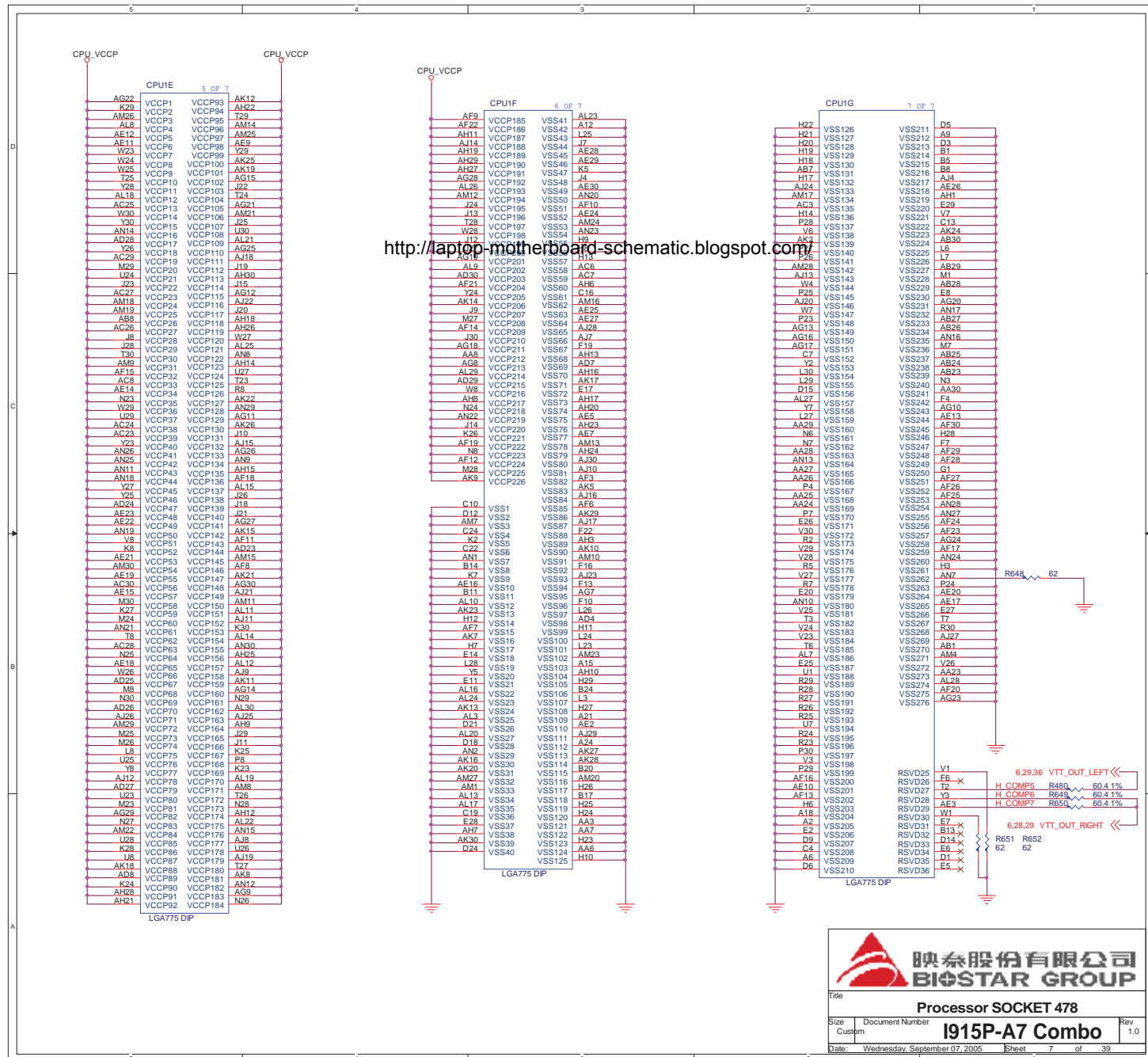
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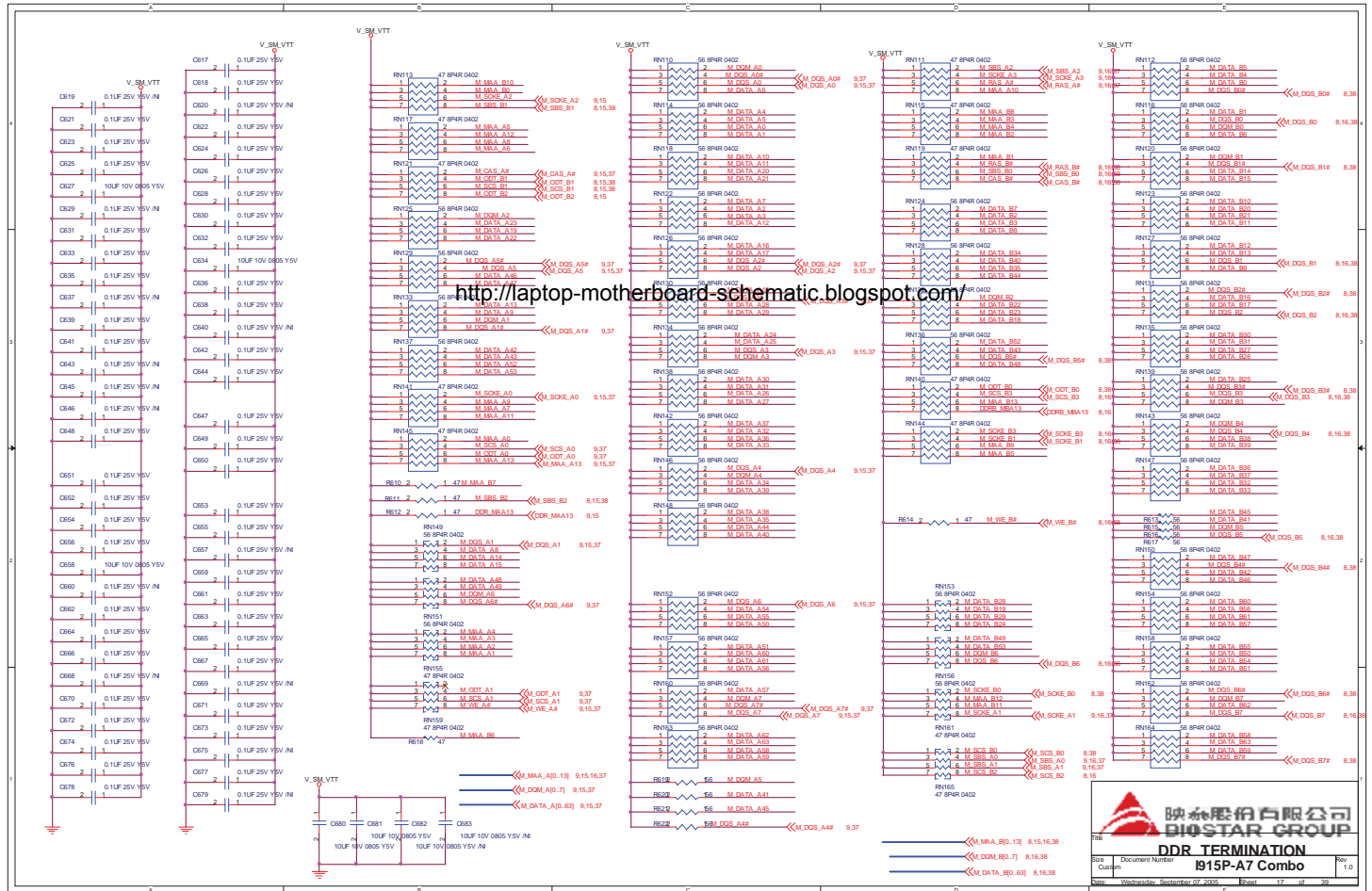
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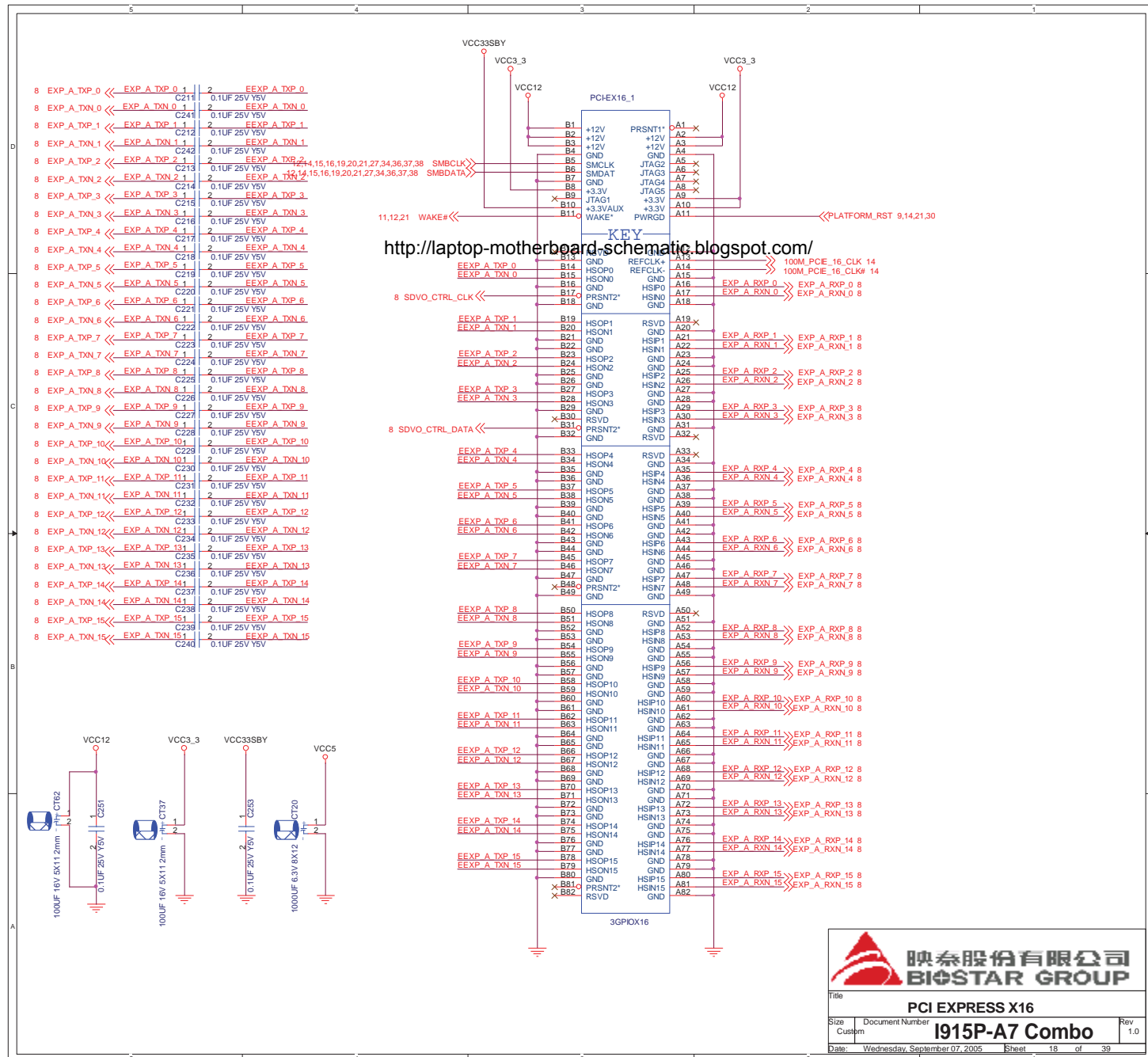
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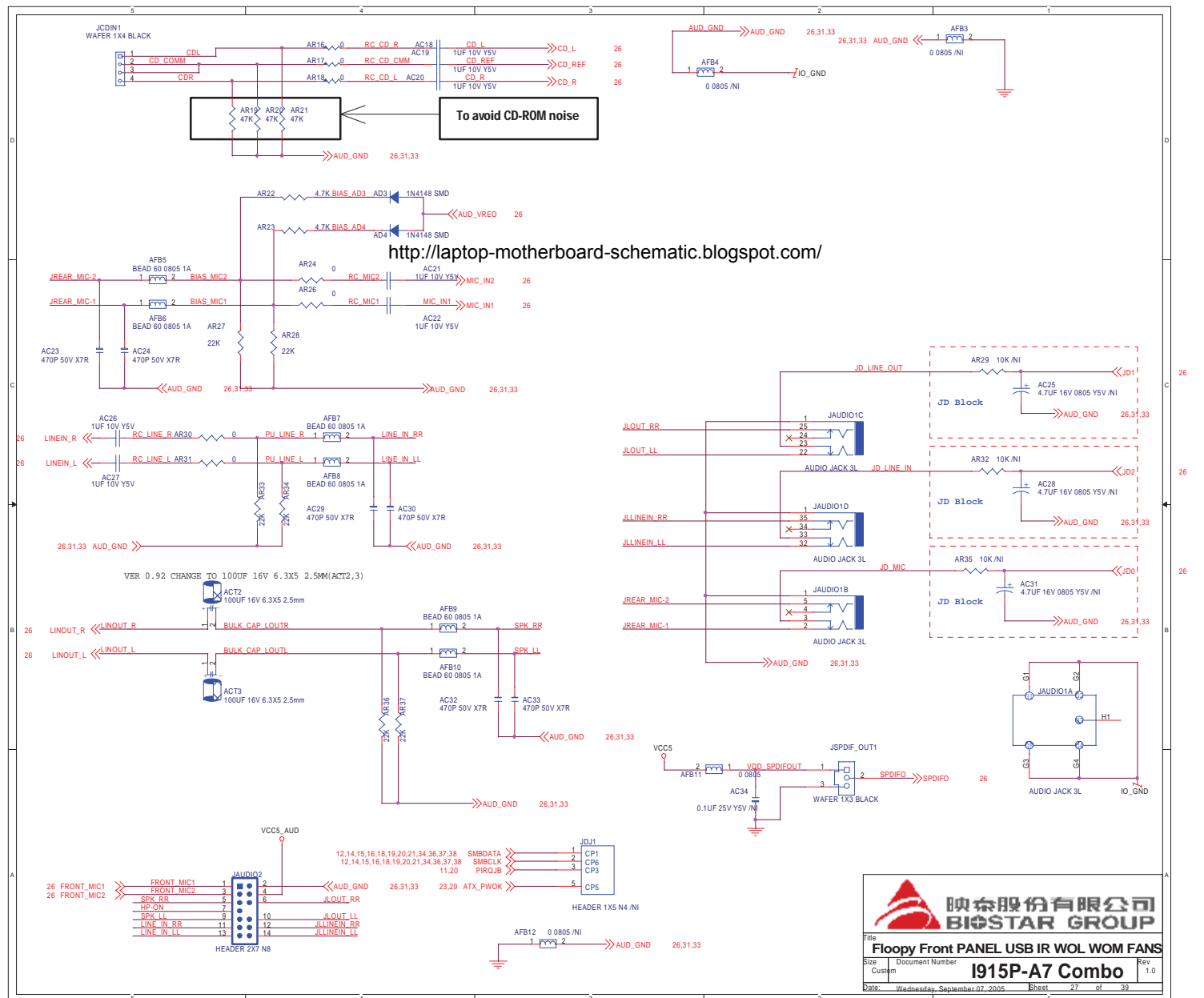
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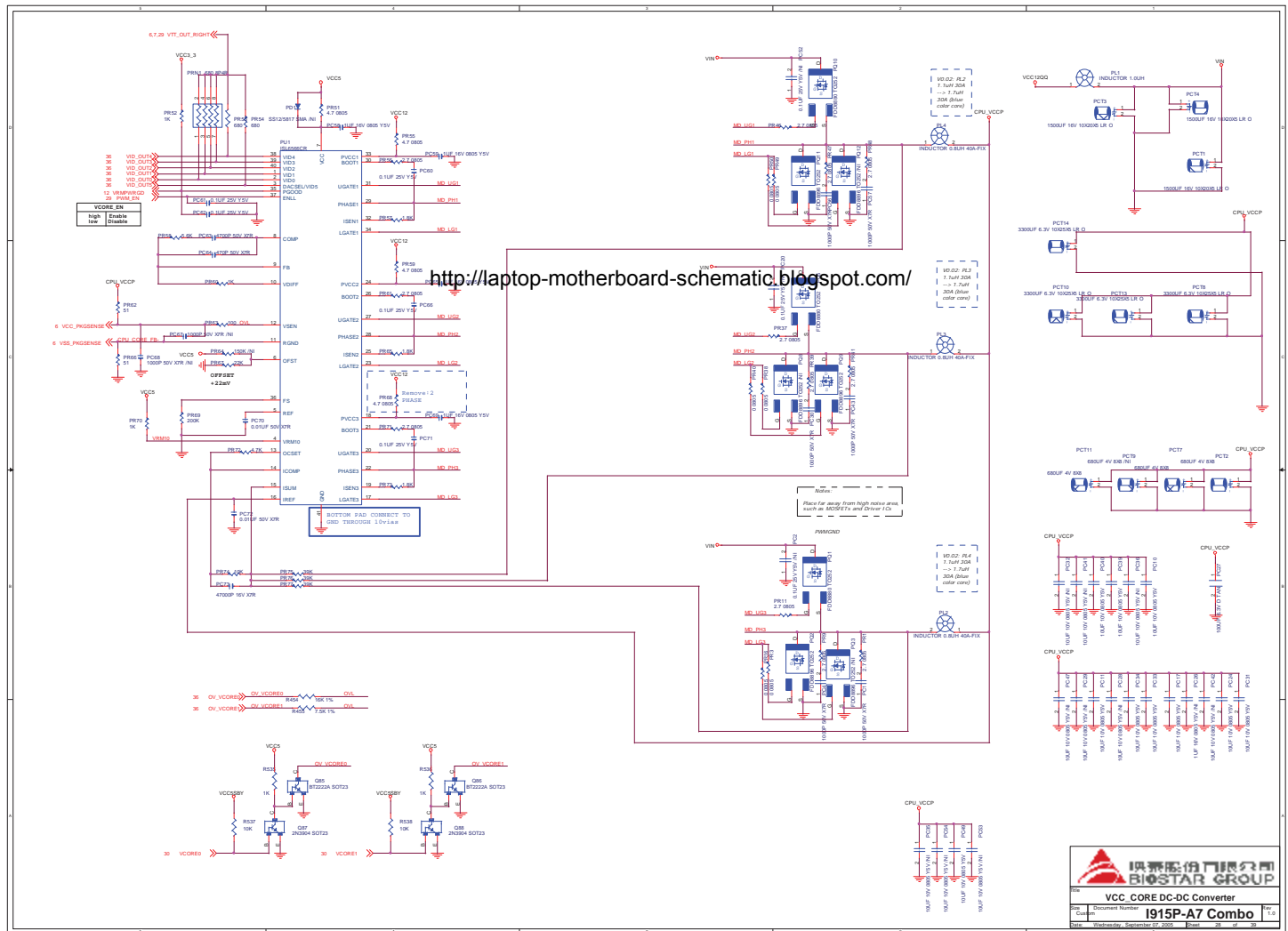











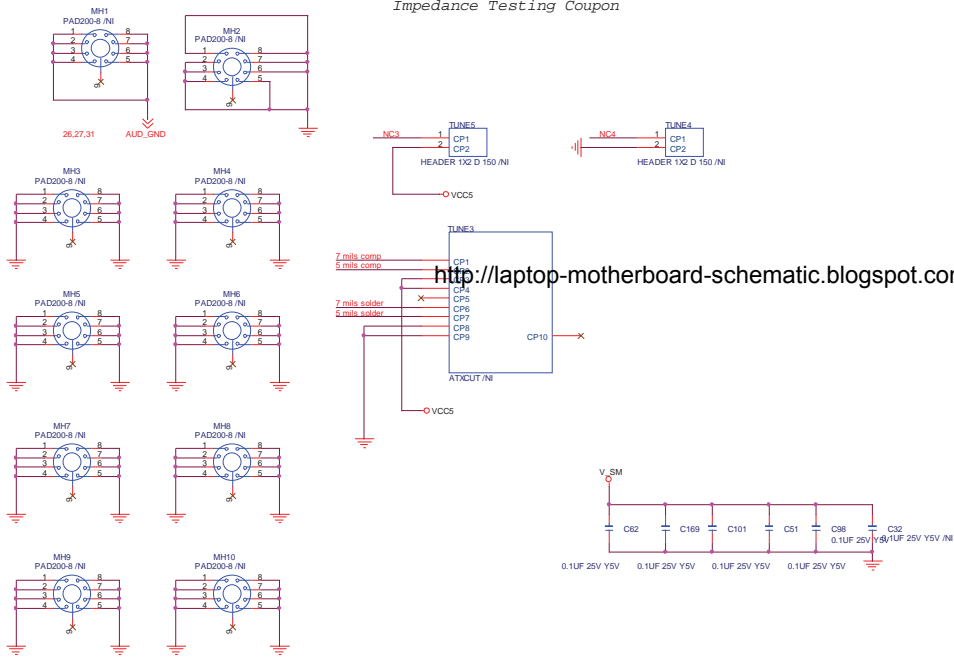


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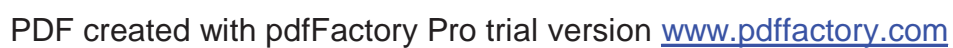
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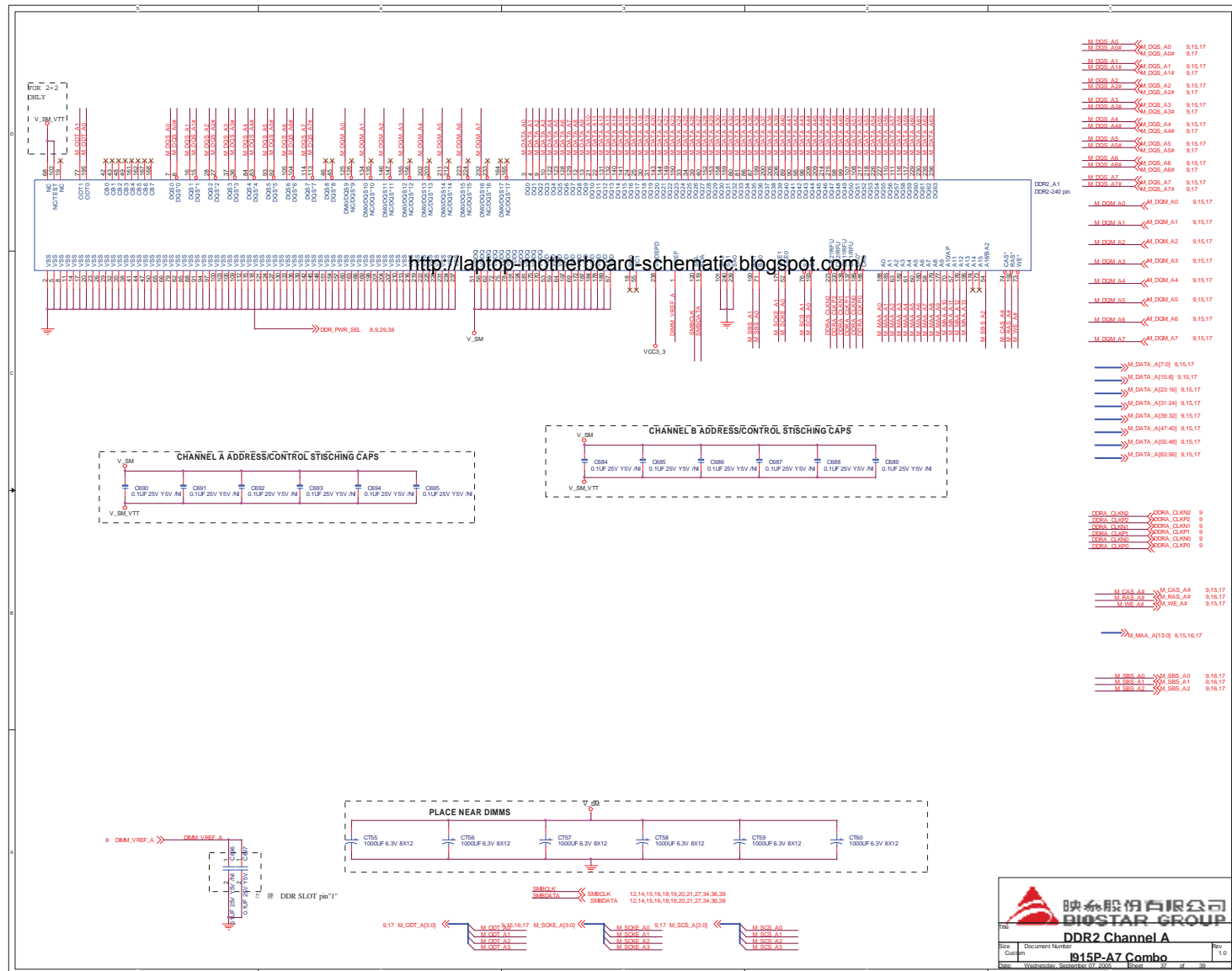
		映泰股份有限公司 BIOSTAR GROUP	
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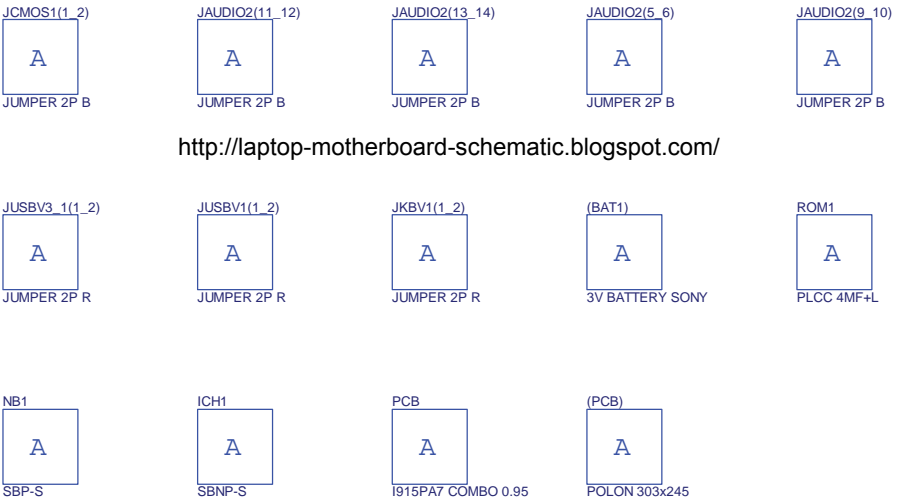
Impedance Testing Coupon




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MH & Coupon	
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